



LA-6762-102.US/09907997

#6

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: SYWE N. LEE

Group Art Unit: 2612

Serial No. 09/433,677

Examiner: Moe, Aung Soe

Filed: November 4, 1999

For: ITNERLACE OVERLAP PIXEL
DESIGN FOR HIGH SENSITIVITY
CMOS IMAGE SENSORS

TRANSMITTAL OF DRAWING CORRECTION

Hon. Commissioner for Patents
P. O. Box 1450
Alexandria, Virginia 22313-1450

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JAN 07 2004

Technology Center 2600

Attn: Official Draftsman

Sir:

Transmitted herewith are corrected drawing (2 sheets) for filing in the above-referenced patent application.

The drawings were objected to on the basis that Figs. 1A and 1B were not designated as prior art. The enclosed drawings now correct this omission.

Favorable reconsideration of the enclosed drawing is respectfully requested.

Respectfully submitted,

FULBRIGHT & JAWORSKI L.L.P.

December 31, 2003

By:

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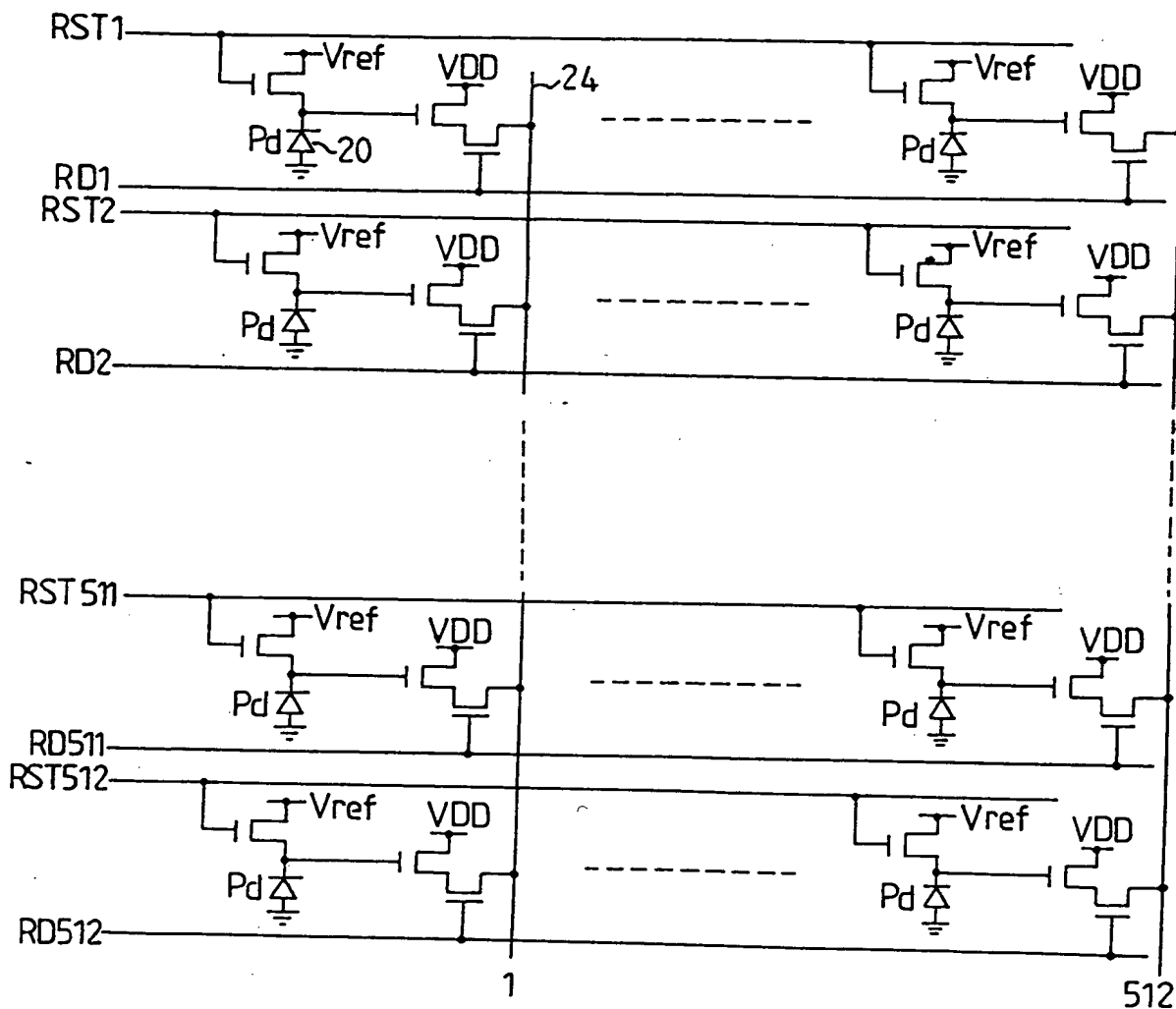


FIG.1 A

(PRIOR ART)

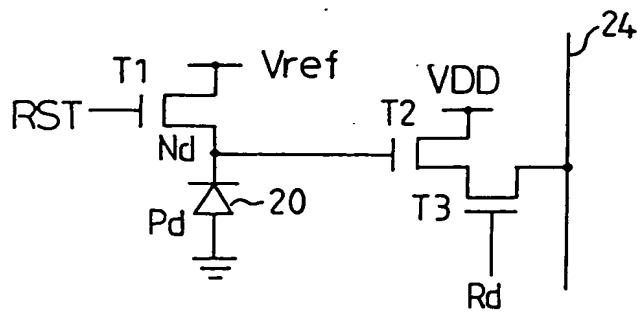


FIG.1B

(PRIOR ART)